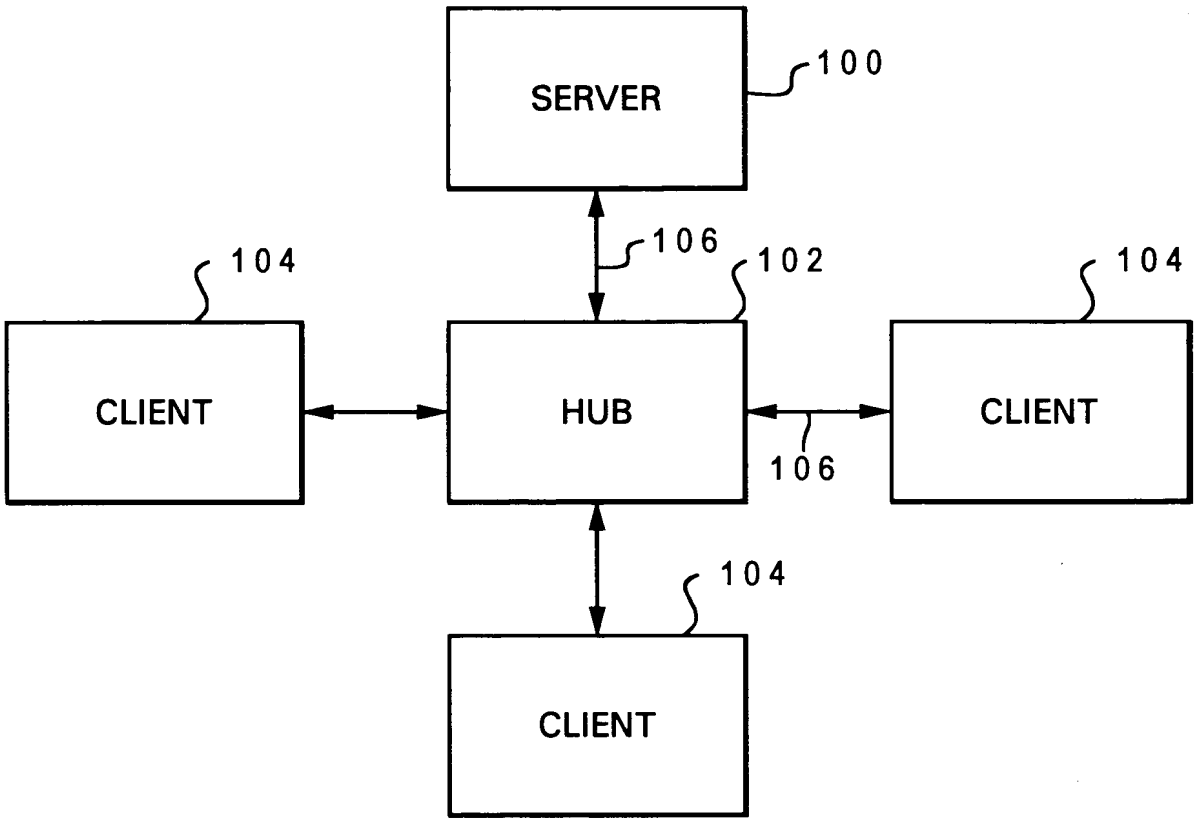

Fig. 1

Patent-5,958,015

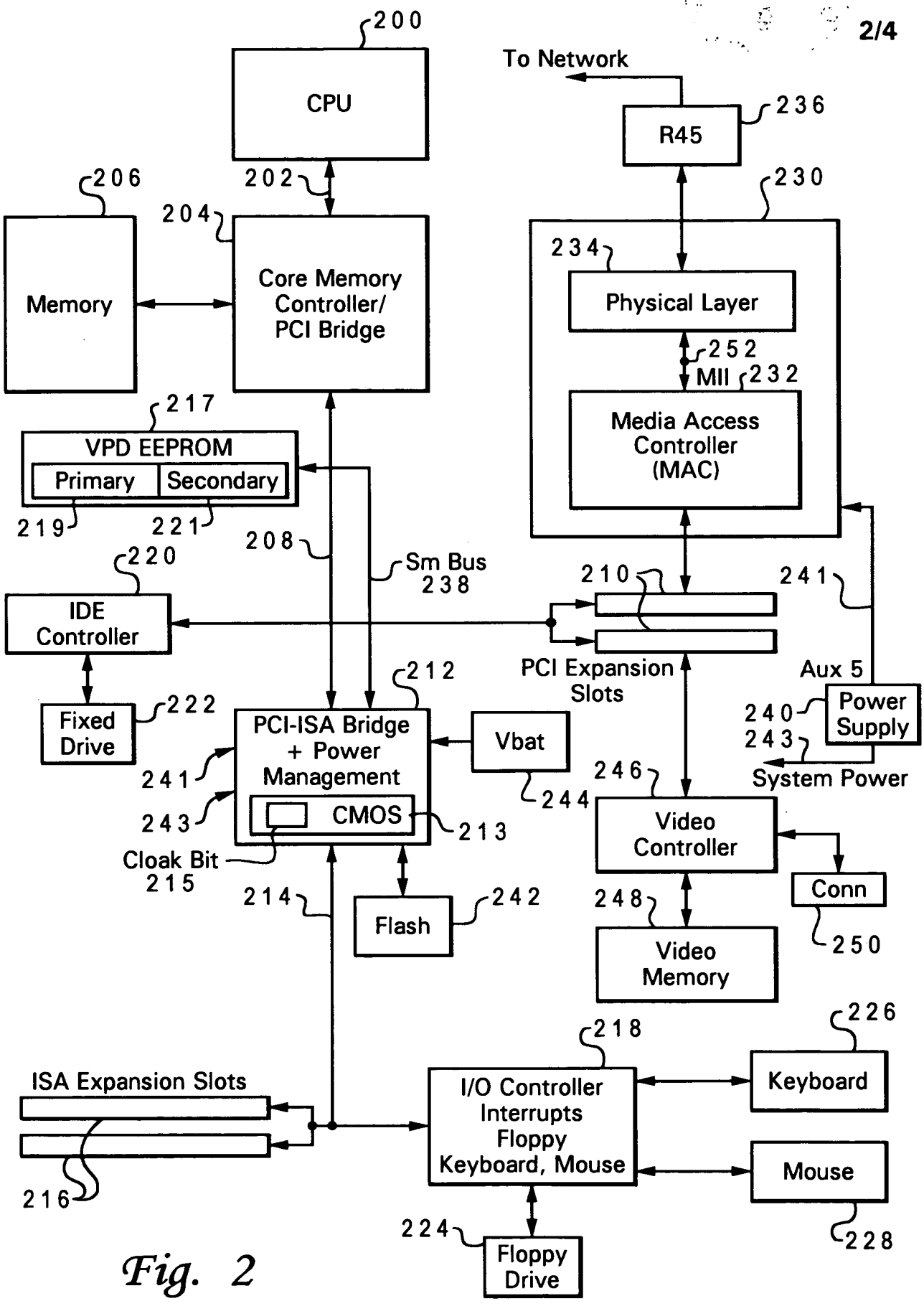
200
CPU
To Network
R45
236
206
202
204
230
Memory
Core Memory Controller/ PCI Bridge
234
Physical Layer
252
MII
232
Media Access Controller (MAC)
217
VPD EEPROM
Primary
Secondary
219
221
208
Sm Bus 238
220
210
241
IDE Controller
PCI Expansion Slots
Aux 5
212
222
Fixed Drive
PCI-ISA Bridge + Power Management
Vbat
240
Power Supply
243
System Power
241
246
243
CMOS
213
244
Video Controller
Cloak Bit 215
214
Flash
242
Conn
248
250
Video Memory
226
218
ISA Expansion Slots
I/O Controller Interrupts Floppy Keyboard, Mouse
Keyboard
Mouse
216
224
228
Floppy Drive

Fig. 2

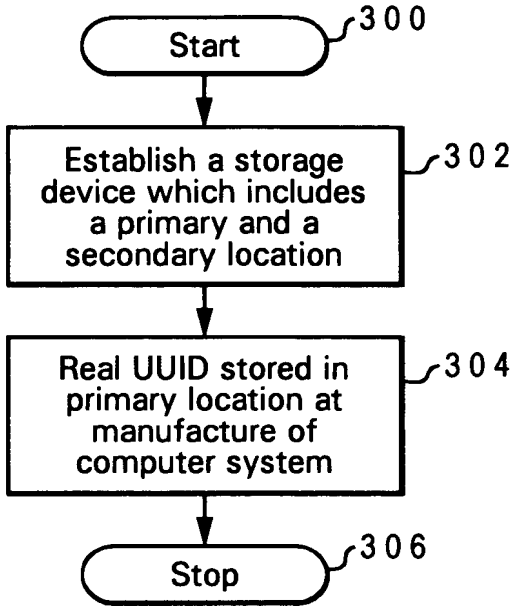
Start
300
Establish a storage device which includes a primary and a secondary location
302
Real UUID stored in primary location at manufacture of computer system
304
Stop
306

Fig. 3

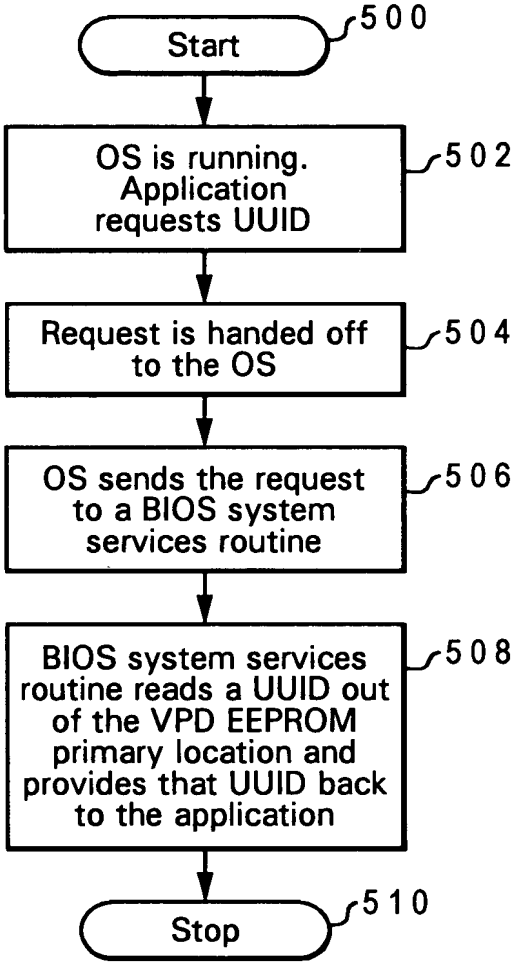
Start
500
OS is running. Application requests UUID
502
Request is handed off to the OS
504
OS sends the request to a BIOS system services routine
506
BIOS system services routine reads a UUID out of the VPD EEPROM primary location and provides that UUID back to the application
508
Stop
510

Fig. 5

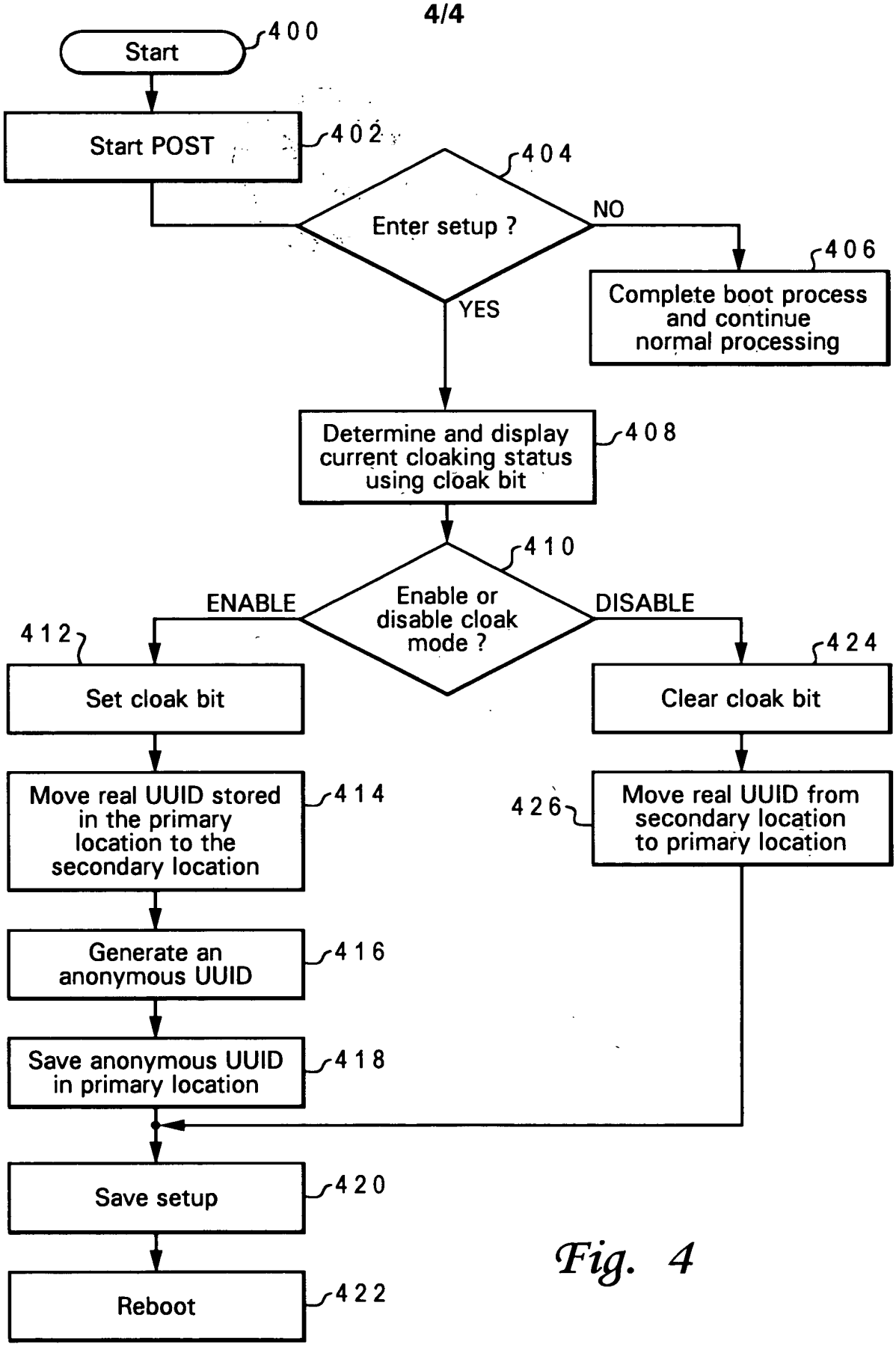
Start
400
Start POST
402
Enter setup ?
404
NO
406
Complete boot process and continue normal processing
YES
Determine and display current cloaking status using cloak bit
408
Enable or disable cloak mode ?
410
ENABLE
DISABLE
412
Set cloak bit
Move real UUID stored in the primary location to the secondary location
414
Generate an anonymous UUID
416
Save anonymous UUID in primary location
418
424
Clear cloak bit
426
Move real UUID from secondary location to primary location
Save setup
420
Reboot
422

Fig. 4